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APPLICANT  
Breitwisch, et al.

FILING DATE  
12/7/2001

GROUP

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
		6,194,258	02/2001	Wuu			
		6,160,282	12/2000	Merrill			
		6,057,215	05/2000	Kitano			
		6,051,864	04/2000	Hodges, et al.			
		6,020,242	01/2000	Tsai, et al.			
		5,973,382	10/1999	Burgener, et al.			
		4,656,731	04/1987	Lam, et al.			
		6,521,529	02-2003	Asamura, Takeshi			
		6,521,952	02-2003	Ker et al.			
		5,633,523	05-1997	Kato, Seiichi			
		5,635,426	06-1997	Hayashi et al.			

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	TRANSLATION	
					YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

R.D. Isaac, "The Future of CMOS Technology", 3 May 2000, Vol. 44, pp. 369-378.

EXAMINER	DATE CONSIDERED

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.